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9th National Power Electronics Conference

NPEC 2019

13th – 15th December





Organised by

Department of
Electrical and Electronics Engineering,
National Institute of Technology
Tiruchirappalli, Tamil Nadu, India
Technical Co-sponsors





About 9th NPEC

The 9th National Power Electronics Conference (NPEC 2019), a biennial conference of Power Electronics forum in India focuses on dissemination of the potential research findings in Industries and academia. It features extensive exposition on Power Electronics through (1) plenary talks, (2) panel discussions, (3) Tutorials, (4) Paper Presentations and so on. NPEC was first organized at IIT, Bombay in 2003 and subsequently in IIT, Kharagpur (2005), IISc Bangalore (2007), IIT, Roorkee (2010), IIEST, Shipur (2011), IIT Kanpur (2013), IIT Bombay (2015) and College of Engineering, Pune (2017) to enhance the interaction of the peers in the field of Power Electronics. This conference will be a launch pad for many researchers to exhibit the influence of Power Electronics on various applications viz. Industry, domestic, commercial, mobility and electric utility systems. Its impact on effective energy conversion from renewable energy sources will be envisaged in the forum. Technical sessions by industrial experts will deliberately bring out the recent challenges faced in industry which will provide an opportunity for inspirational minds to solve them. This will lead to a sturdy power electronic community comprising of academicians, research scholars and industry professionals. The development of new circuits and tools for the power electronic systems will be outcome of this conference.

The 9th edition of this prestigious conference is to be held at National Institute of Technology, Tiruchirappalli, an institute of national importance which is situated in the banks of river Cauvery is the top amongst NITs that holds 11th rank in the NIRF rankings.

It would be another proud moment for this institute to organize this conference which will be an official get-together for the power electronics community. The theme for this conference would be "Effective green energy conversion and its application to the utilities" which will cover, but not limited to the following topics:

Themes

Track - 1	Drivoc	and Contro	ı
irack – i	Drives	and Contro	

Track - 2 **Renewable Energy Systems**

Track - 3 Power flow control

Power Converter Topologies, Components and Track - 4 Devices

Track - 5 **Power Engineering Education and Policy Issues**

Important Dates

: February 17, 2019 Online Paper submission opens July 15, 2019 July 31 Last date for paper submission : August 15, 2019 Last date for notification of acceptance Last date for submission of final paper September 1, 2019 Online/early bird registration starts : September 1, 2019 Submission of camera ready paper September 15, 2019 Last date for early bird registration October 1, 2019 Last date for online registration : October 30, 2019

Conference Registration (including GST)

Type of Registration	Early bird registration (up to 1 st Oct.)	Registration after 1st Oct.
Delegates from Industries/ Utilities/R&D organizations	Rs. 10,000/-	Rs. 12,000/-
Delegates from academic institutions	Rs. 7,000/-	Rs. 8,000/-
Student delegates	Rs. 3,500/-	Rs. 4,500/-
Delegates from abroad	US\$ 400	US\$ 500

Note: At least one author has to register online before the last date for the submission of camera-ready paper. A discount of 20% will be conference proceedings will be included in IEEE Explore, only if one of the authors registers and presents the paper in the conference.

Transactions on Industrial Applications

Information for Authors

The authors have to submit the full paper in the IEEE double column format, restricted to a length of six pages. Detailed instructions and URL: http://npec2019.nitt.edu/

Sponsorship

Financial sponsorships for NPEC – 2019 are invited under the following categories. Prospective sponsors are requested to visit the conference website http://npec2019.nitt.edu/ to get more details about the type of sponsorship and the associated benefits.

Platinum Sponsor : Rs. 5 Lakhs Gold Sponsor : Rs. 4 Lakhs : Rs. 3 Lakhs Silver Sponsor Bronze Sponsor : Rs. 2 Lakhs

About EEE Department, NIT Tiruchirappalli

Started in the year 1964, the Department of Electrical and Electronics Engineering, offers one undergraduate programme in EEE, two postgraduate programmes (Power Systems & Power Electronics) and

research degrees (M.S. & Ph.D.) in various fields of Electrical and Electronics Engineering. The Department is recognized for leadership in research, teaching and service to the profession.

About NIT Tiruchirappalli

The National Institute of Technology (formerly known as Regional Engineering College) Tiruchirappalli, was started as a joint and cooperative venture of the Govt. of India and the Govt. of Tamil Nadu during 1964 to meet the needs of technical man-power in this region of the country. The institute was recognized as an Institute of National Importance by the Government of India under the National Institutes of Technology Act, 2007. NIT Trichy has emerged as one of the leading institutions in the field of technical education with excellence in academics, research, industry connect, social engagement and financial management. Ranked by NIRF 2018 as one among the top 50 institutions, occupying the 11th position under technical institutions, NIT Trichy leads the rankings among 31 NITs in the country. At the regional level, NIT Trichy also features in the top 250 positions in QS Asia Rankings 2018. Today, NIT Trichy offers 10 undergraduate and 27 postgraduate programs and research degrees. A salubrious campus with excellent academic departments, dedicated teachers and cream of students drawn across the country renders the institute to be the dream destination for all stakeholders.

About Tiruchirappalli

available for online registration of IEEE members. Papers from the Tiruchirappalli (known as Trichy) is situated in the centre of the state of Tamil Nadu, on the banks of the river Cauvery. The city is the administrative headquarters of Tiruchirappalli District. Trichy 20% of the presented papers qualify for submission into IEEE encompasses Srirangam, the foremost Vaishnavite temple and the largest functioning Hindu temple in the world. "Thiruanaikka" (Thiruvanaikkaval), located very close to Srirangam temple and one of the five main abodes ("Panchaboodha Sthalam") of Lord Shiva, is the House of Water (Jalalingam). Tiruchirappalli is home to many other guidelines regarding paper submission are available at the following historic temples, monuments, churches and mosques. With over 2.500 years of known history, it was the capital of the early Cholas. It was an important town in the days of the later Cholas, Navaks, and also during the early days of the British East India Company. Considered an educational town, Tiruchirappalli has a few schools and colleges that are older than hundred years. Trichy is the home to several prominent educational institutions such as Bharathidasan University, National Institute of Technology, the Government Law College, Bharathidasan Institute of Management, Indian Institute of Management, K.A.P.V. Medical College and St. Joseph's College where former Indian President Dr. A.P.J. Abdul Kalam graduated. There are some major industrial houses in Trichy such as BHEL (Bharat Heavy Electricals Limited), Ordinance Factory, and Railway Workshop. The city is well connected by rail, air and road to the rest of the country and enjoys a very pleasant weather in the month of December.